Comprehensive Cybersecurity Technology for Critical Power Infrastructure AI-Based Centralized Defense and Edge Resilience



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Cyber Security News





CSIS, Significant Cyber Incidents, https://www.csis.org/programs/strategic-technologies-program/significantcyber-incidents

Cyber-Attack Resilience for CPS – Part A

Goal:

- Continuous, uninterrupted operation under direct cyber-attack campaign
- Develop an economical and robust cyber attack resilience at controller level (level 0 & 1), relying on the physical properties of the controlled physical systems.
- Past Methods:
- BFT++ is a new approach to resiliency, leveraging established Fault Tolerant systems.
- Proposed SubProcess BFT++ will reduce the deployment cost for BFT++ cyber-attack-resilience.



Cyber-Attack Resilience for CPS – Part B



We plan to integrate the SubProcess BTF++ engineering tool into Schweitzer Engineering Laboratories (SEL) PLC design tools and environment.

Impact:

- Providing cyber attack resilience for application which cannot afford device redundancy, alleviate the need for redundant device in BFT++,
- Significantly widen the applicability of BFT++ and resilience against direct cyber-attack
- Automated isolation of offending data, can be communicated to other system components, e.g. SCATOPSY, RAM2., to prevent repeat attack.
- Integration into SEL design environment for ease of deployment and dissemination.

Cyber-Attack Resilience for CPS – Progress

Current status:

- Coordination meetings with SEL:
 - Initial Coordination meeting on April 5th 2022
 - Training for SEL RTAC development tools and environment on April 18th 2022
 - Meeting for further deep dive into SEL's:
 - Operating System implementation
 - Compiler and code generation process
 - Real time scheduler
 - Etc.
 - is currently being scheduled
- Commercialization through integration into Vendor's development environment (SEL, and other vendors).

- Started on May 1st 2022
- Initial research will use an open source PLC environment: **ClassicLadder**.
 - For experimentation platform and
 - For analyzing generated codes for PLCs
 - Understanding scheduling structure
 - Studying design trade offs for integrating sub-process BFT++
 - A Linux toolset, as oppose to Windoze
- We'll revisit this decision after SEL RTAC deep dive.
- Future: integration into SEL design tools and environment

Schedule & Milestones:



- We are starting in May 2022
- Team:
 - Dr. Sukarno Mertoguno
 - Dr. Michail Alexiu
 - Martin Ivanchev, joining in August 2022

The End



Cartoon Songs From ERRIEMELODIES & LOONEY NUNER.

Cyber

Physical

System

Physics Rules !!!