

## **Michael Harash, M.Sc.**

**Project Role:** Algorithm Development

**Affiliated Organization:** SIGA OT Solutions

**Job Title:** Head of Algorithm Development

### **Relevant Job Experience:**

Mr. Harash is an Algorithm Developer with a strong mathematical background and experience in Digital Signal Processing and Machine Learning. Passionate about data science, new technologies and software. Michael Holds B.Sc. and M.Sc. in Electrical and Computer Engineering both in Ben Gurion University. His interests and research were Signal and Image Processing, Speech Recognition, Machine Learning, Natural Language Processing, Computer Vision, Probability Theory, Statistics, Neural Networks. Prior to SIGA, Michael worked as Algorithms Developer in EchoCare Technologies (SU company – Health Care field).

### **Significant Accomplishments:**

2018-Present: Head of Algorithms Development, SIGA OT Solutions

- Develop algorithms for anomaly detection in multi-dimensional time series data.
- Develop algorithms for predictive maintenance.

2015 –2018: EchoCare Technologies (SU company – Health Care field), Algorithm Developer:

- Develop and implement algorithms for home-monitoring RADAR system, capable of extract human posture, motion (Fall-Detection), location and respiration from a radar signal. Develop feature-extraction and classification scheme using both signal processing & machine learning algorithms.
- Design system architecture.
- Develop image reconstruction algorithm (back-projection) from a radar signal.

2014 –2015: Ben-Gurion University (During M.Sc. degree):

- Teaching Assistant at "Digital Systems" course.
- Laboratory guide at "Introductory Electricity Laboratory" and "Analog Circuits Laboratory".

2013 -2014: Intel, Software Developer (student position):

- Development of firmware and software validation.

2012 –2013: Elta Systems, Hardware Engineer (student position):

- Development of security chip. Design two hardware components capable of communicating each other using encrypted communication to authenticate and protect Elta's intellectual properties. Implementation over FPGA in VHDL.

### **Education:**

2009-2013: B.Sc. in Electrical & Computer Engineering. Signal Processing & Communication track. Ben Gurion University.

2013-2015: M.Sc. in Electrical & Computer Engineering. Signal Processing track. Ben Gurion University.

**Professional Affiliations and Committee Memberships:** None to report.